L Number	Hits		DB	Time stamp
1	134	"semiconductor chip" and "memory chip" and	USPAT;	2004/07/20 14:46
		"peripheral circuit" and dedicat\$3	US-PGPUB;	
			EPO; JPO;	
	,		DERWENT;	
			IBM TDB	
2	2	("semiconductor chip" and "memory chip" and	USPAT;	2004/07/20 14:48
] -	-	"peripheral circuit" and dedicat\$3) and	US-PGPUB;	2000, 01, 20 2011
	1	"first chip" and "second chip"	EPO; JPO;	
		libe emp and become emp	DERWENT;	
			IBM TDB	
3	5	("semiconductor chip" and "memory chip" and	USPAT:	2004/07/20 14:48
ا	] 3	"peripheral circuit" and dedicat\$3) and	US-PGPUB;	2004/07/20 14:40
			EPO; JPO;	
1		"design rule"	1 '	
			DERWENT;	
	2	("5060303") 777	IBM_TDB USPAT;	2004/06/11 15:31
-		("5969383").PN.	1	2004/06/11 15:31
			US-PGPUB;	
			EPO; JPO;	
	ļ		DERWENT;	
	_	(,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	IBM_TDB	
-	2	("5969383").PN.	USPAT;	2004/06/10 13:13
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
		(,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	IBM_TDB	
-	4	(("6741486") or ("6594169")).PN.	USPAT;	2004/07/20 07:35
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	4944	"first memory" and "second memory" and	USPAT;	2004/07/20 07:37
		peripheral	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	66	"first memory" same "second memory" same	USPAT;	2004/07/20 09:57
		"peripheral circuit"	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	5	"first memory" same "second memory" same	USPAT;	2004/07/20 10:37
		peripheral same dedicate\$3	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1		USPAT	2004/07/20 10:03
-	1		USPAT	2004/07/20 10:05
	1		USPAT	2004/07/20 10:05